

**EAST - [10501430.wsp:1]**

File View Edit Tools Window Help

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☐ L2: (4) nonvolatile near ((two-transistor two near transistor) with memory)  
☐ L3: (7) nonvolatile near ((two-transistor two near transistor)) and memory  
☐ L1: (485) nonvolatile and ((two-transistor two near transistor) with memory)  
☐ L4: (93) nonvolatile and (two-transistor two near transistor) and selection near transistor and m...  
☐ L5: (220) nonvolatile and ((two-transistor two near transistor) with memory) and masks3  
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 DBs US-PGPUB; USPAT; USOCR; EPI Plurals  
 Default operator: OR Highlight all hit terms initially

nonvolatile and (two-transistor two near transistor) and selection near transistor and memory near transistor

BRS form ISIR form Image Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
51	<input type="checkbox"/>	<input type="checkbox"/>	US 6579762 B2	20030617	37	Nonvolatile semiconductor device having a memory cells each of which is constituted of a memory transistor and a selection transistor	438/258	257/314; 257/E21.691
52	<input type="checkbox"/>	<input type="checkbox"/>	US 6566707 B1	20030520	108	Transistor, semiconductor memory and method of fabricating the same	257/316	257/327; 257/762; 257/E21.682
53	<input type="checkbox"/>	<input type="checkbox"/>	US 6549449 B2	20030415	32	Semiconductor device	365/149	365/145; 365/230.06
54	<input type="checkbox"/>	<input type="checkbox"/>	US 6531735 B1	20030311	85	Semiconductor integrated circuit	257/324	257/314; 257/E21.679; 257/E27.103
55	<input type="checkbox"/>	<input type="checkbox"/>	US 6518617 B1	20030211	18	Nonvolatile semiconductor memory device	257/314	257/324; 257/E29.309
56	<input type="checkbox"/>	<input type="checkbox"/>	US 6493271 B2	20021210	92	Data line disturbance free memory block divided flash memory and microcomputer having flash memory therein	365/185.33	257/E21.683; 257/E21.689; 257/E27.103
57	<input type="checkbox"/>	<input type="checkbox"/>	US 6477671 B2	20021105	56	Semiconductor memory, memory device, and memory card	714/718	365/195; 365/201; 714/54
58	<input type="checkbox"/>	<input type="checkbox"/>	US 6459110 B1	20021001	10	Semiconductor storage element	257/295	257/E27.104; 365/145; 365/182
59	<input type="checkbox"/>	<input type="checkbox"/>	US 6414878 B2	20020702	92	Data line disturbance free memory block divided flash memory and microcomputer having flash memory therein	365/185.33	257/E21.683; 257/E21.689; 257/E27.103
60	<input type="checkbox"/>	<input type="checkbox"/>	US 6400609 B1	20020604	92	Data line disturbance free memory block divided flash memory and microcomputer having flash memory therein	365/185.28	257/E21.683; 257/E21.689; 257/E27.103
61	<input type="checkbox"/>	<input type="checkbox"/>	US 6335879 B1	20020101	93	Method of erasing and programming a flash memory in a single-chip microcomputer having a procession unit and memory	365/185.08	257/E21.683; 257/E21.689; 257/E27.103